

MODIFYING A WORD CLOCK INPUT FOR LOW LEVEL SOURCE

The DCD-8 has 3 Word Clock inputs, 2 on the rear panel and 1 on the front. The required level for these inputs is 0.79Vp-p.

When using a 10MHz GPS signal as a reference, under most circumstances, the signal level into the DCD-8 will be fine and no modification is required. However, on occasion, it may be too low.

Sometimes also the level of the source word clock may also be too low.

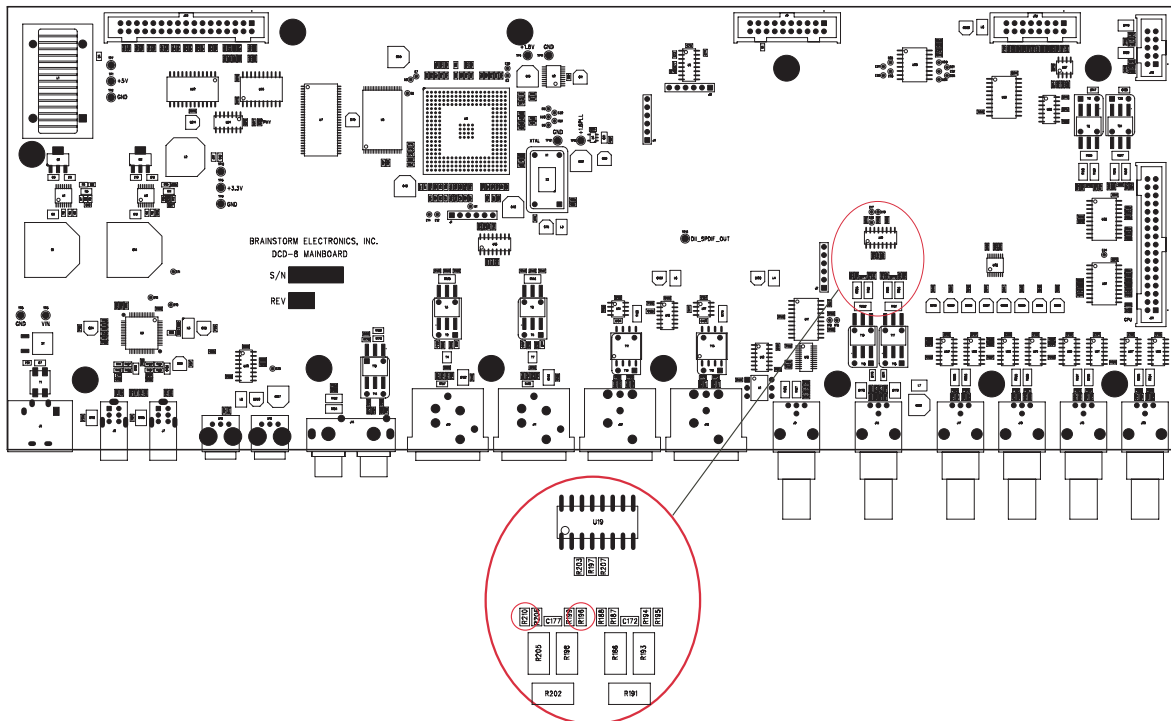
For those situations, 2 resistor values can be changed so that the required level be only 0.59Vp-p.

MODIFICATION:

Typically, we would recommend modifying WC Input 2 only, leaving the other 2 inputs unchanged.

To modify Input 2:

- locate R196 and R210. Normal value for both resistors is 4.7K Ω .
- Replace them by 10.5K Ω resistors.



Inputs 1 & 3 can be modified the same way. The resistors to change for those inputs are:

- INPUT 1: R188 & R195
- INPUT 3: R214 & R221